

## **Information about 16.480/552 Exam 2 on Dec 10, 2014**

90-minutes exam (6:30pm-8:00pm). immediately followed by lab session and demos

Similar format as Exam 1

- Multiple Choices (Type A): 5 questions, 4 points each
- Multiple Choices (Type B): 5 questions, 4 points each
- Short answer questions: 5 questions,  $10 \times 4 + 20$  points

Coverage (For review only. Not an exact or complete list)

- DMA: Signals involved in DMA, purpose/benefits of DMA
- Interrupt: Purpose/benefits of interrupt, signals involved for x86 processors, interrupt vector numbers
- PCI/PCIe: features, configuration space, PCI bus in a computer system, identification of a PCI device
- I<sup>2</sup>C bus: hardware architecture, addressing, read/write transactions, lab 3 design
- Parallel programming: multithreaded programs, design methodology, Lab 4 design
- Linux: Linux features, device driver concept, usage, Yocto project
- Bus protocol: timing diagrams, Lab 2.
- One of the emerging technologies in ESD.